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HE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Patent Application of

Byeung-Joon AHN, et al.

Date:

October 28, 2003

Serial No.:

09/915,703

Group Art Unit:

1762

Filed: July 26, 2001

Examiner:

Ljiljana V. Ciric

For:

METHOD FOR FABRICATING SURFACE MOUNTABLE CHIP INDUCTOR

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **SUBMISSION**

Sir:

Submitted herewith is a copy of art together with a form listing the same for the convenience of the Examiner.

I certify that each item of information contained in this Submission was cited in a communication from a foreign patent office in a counterpart foreign application not more than 3 months prior to the filing of this Submission. An English equivalent or complete translation of the Japanese references was not available in all instances.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450, on October 28, 2003

Respectfully submitted,

Max Moskowitz

Name of applicant, assignee or Registered Representative

> Signatur October 28,

Date of Signature

MM:cg Enclosures Max Moskowitz

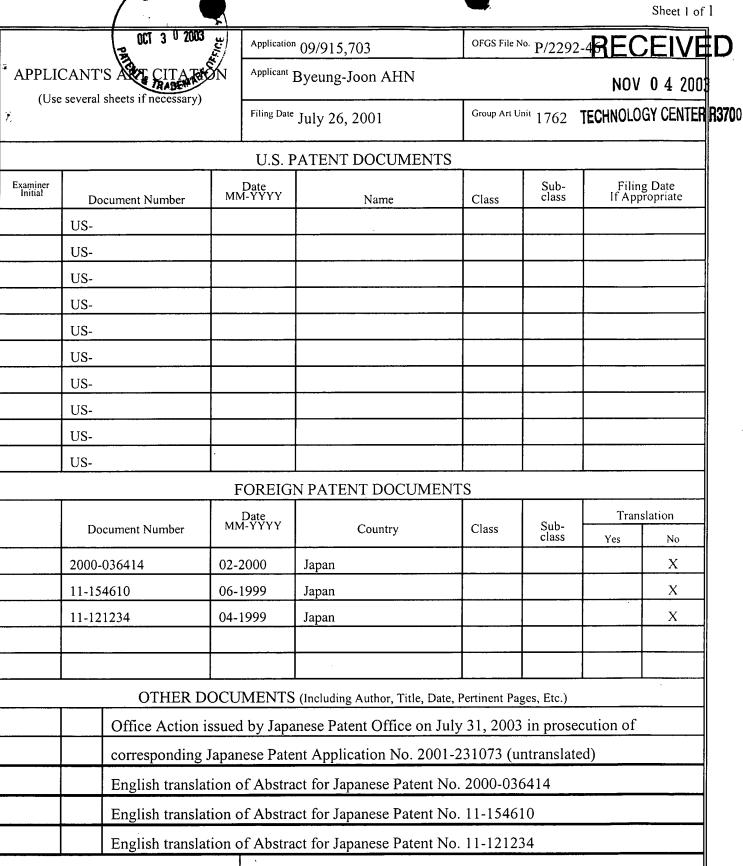
Registration No.: 30,576

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Date Considered

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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Examiner